

**Search Notes**

Application/Control No.

10/052,277

Applicant(s)/Patent under  
ReexaminationJENSEN, JENSEN  
HARTRUNG

Examiner

Christopher E. Lee

Art Unit

2112

**SEARCHED**

Class	Subclass	Date	Examiner
710	305	6/14/2004	CEL
713	300	6/14/2004	CEL
716	6	6/14/2004	CEL
713	322 323	7/28/2005	CEL
327	93 534	7/28/2005	CEL
713	322	2/3/2006	CEL
Above	UPDATED	9/6/2006	CEL

**INTERFERENCE SEARCHED**

Class	Subclass	Date	Examiner

**SEARCH NOTES  
(INCLUDING SEARCH STRATEGY)**

	DATE	EXMR
EASE Search - Bus Control, Gated clock logic, Power saving bus logic & its control	6/14/2005	CEL
EAST - Activity Detector and Enabling signal therefrom for bus controller	7/28/2005	CEL
EAST - (USPT, JPO, EPO, PGPUB, IBM TDB); power management for digital devices See Search History printout.	2/3/2006	CEL
Search Updated	9/6/2006	CEL